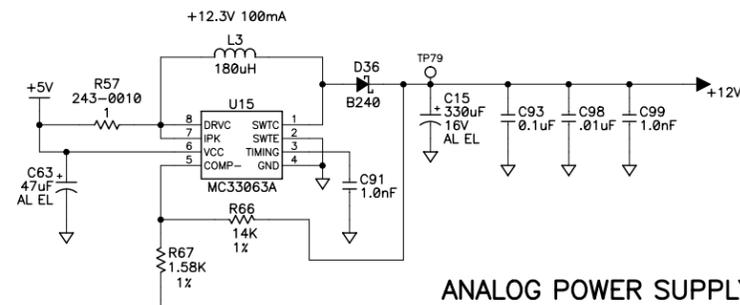
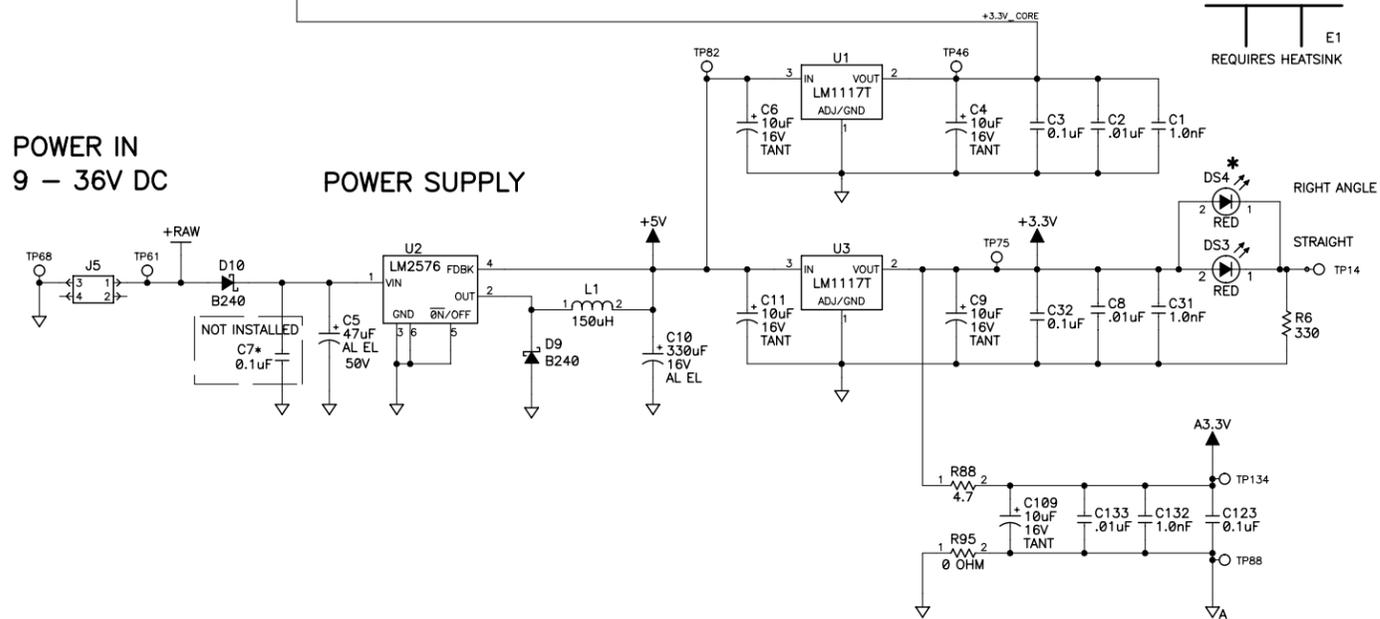


POWER IN
9 - 36V DC

POWER SUPPLY



ANALOG POWER SUPPLYS

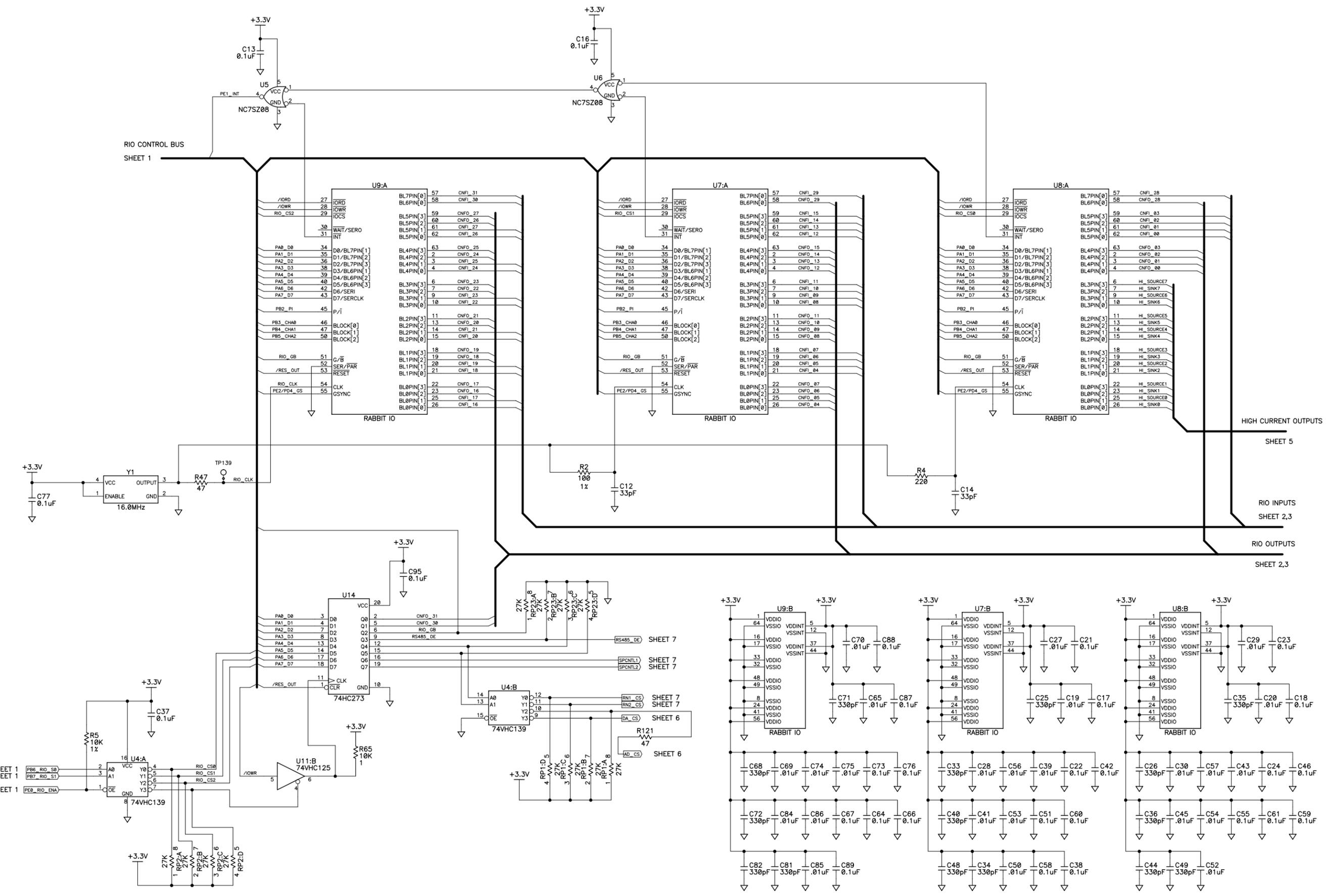
PRODUCT	MODEL #	FEATURES	MORE INFORMATION
BL4S200 SERIES RABBIT SBC	BL4S200	E-NET + SD CARD USING RCM4310	INDIVIDUAL RCM CORE SCHEMATICS CAN BE FOUND AT WWW.RABBIT.COM
	BL4S210	10BASE-T USING RCM4010	
	BL5S220	802.11B/G USING RCM5400W	
	BL4S230	ZB USING RCM4510	

- NOTES: UNLESS OTHERWISE SPECIFIED;
- ALL RESISTOR VALUES ARE IN OHMS, 1/16W, 5% UNLESS LABELED 1%
 - THE ORIGATION SOURCE OF A VOLTAGE IS REPRESENTED BY (VCC), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (VCC).
 - COMPONENT VALUES SHOWN WITH AN ASTERISK (*) FOLLOWING THE VALUE OR DASHED BOX, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED.

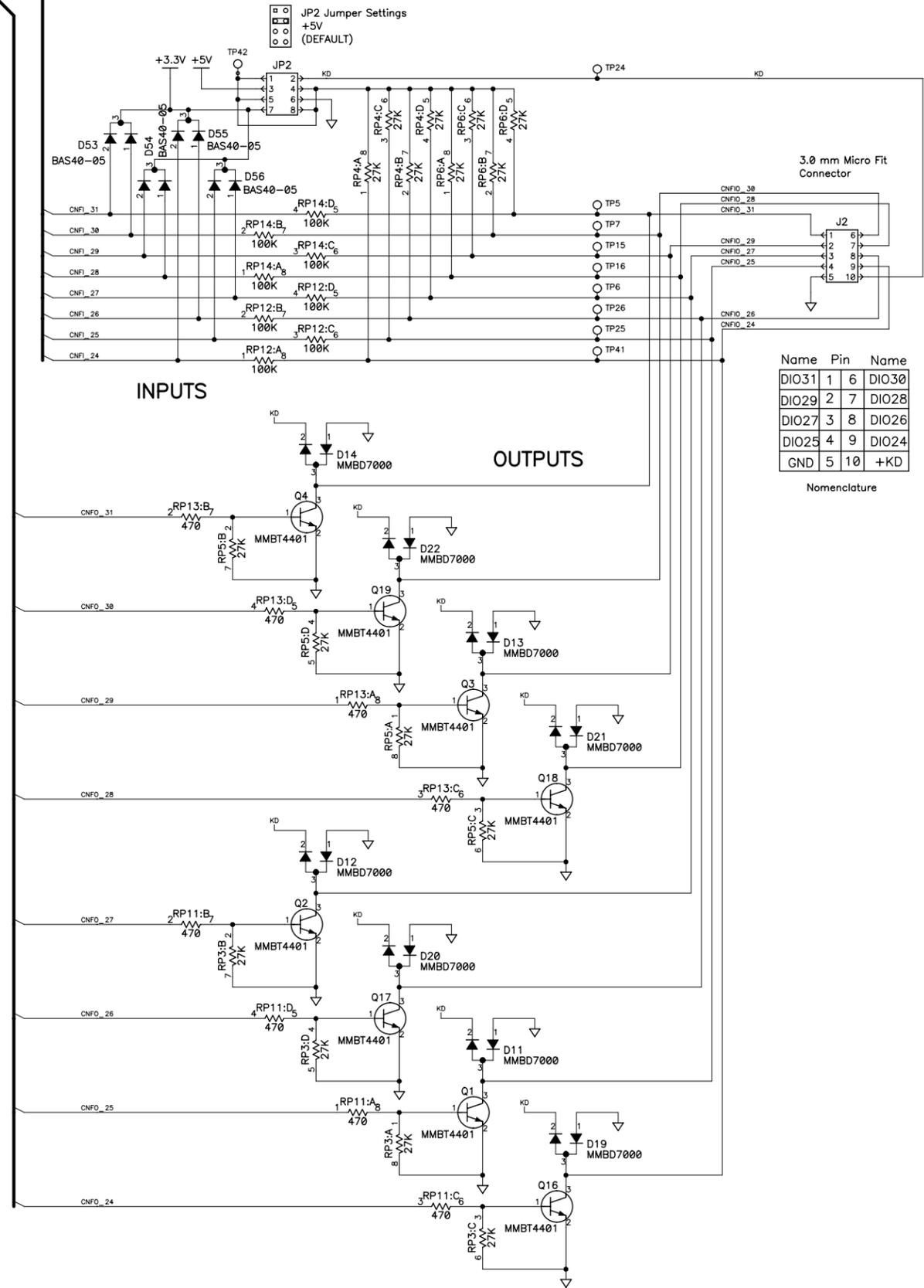
REV	ECO	DESCRIPTION OF CHANGE	REVISION APPROVAL			
			PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
A		INITIAL RELEASE				
B	E15530	REPOSITIONED CAPS TO MATCH PCB	MMK			
C	15683	ADDED TEST POINTS FOR MFG	MMK			
D	000157	ADDED DIODES FOR PROTECTION	MMK			
E	000387	ADDED NOMENCLATURE TO MATCH SAMPLE PROGRAMS	MMK	7/16/08		
F	000404	CHANGED FILTER CAPS AND FEEDBACK RES ON U18	MMK	7/23/08		
G	001379	REMOVED C7 FROM BOM, MARKED AS NOT INSTALLED	XT	7/31/09		

APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:		DRAWING CONTENT:		TITLE	RABBIT SEMICONDUCTOR
		DRAWN BY: (INITIAL RELEASE)	11/9/07		
		M. Knobloch			
		REVISED BY:	7/31/09	APPROVALS: INITIAL RELEASE	SIZE
		X TRUONG			
		PROJECT ENGINEER:	05/19/08	DWG NO.	C
		M. Knobloch			
		ENGINEERING MANAGER:	05/19/08	090-0267	SCALE NONE
		X. Truong			
		SIGNATURES	DATE	RELEASE DATE	SHEET 1 OF 7

RIO CONTROL BUS
SHEET 1



HIGH CURRENT OUTPUTS
SHEET 5
RIO INPUTS
SHEET 2,3
RIO OUTPUTS
SHEET 2,3



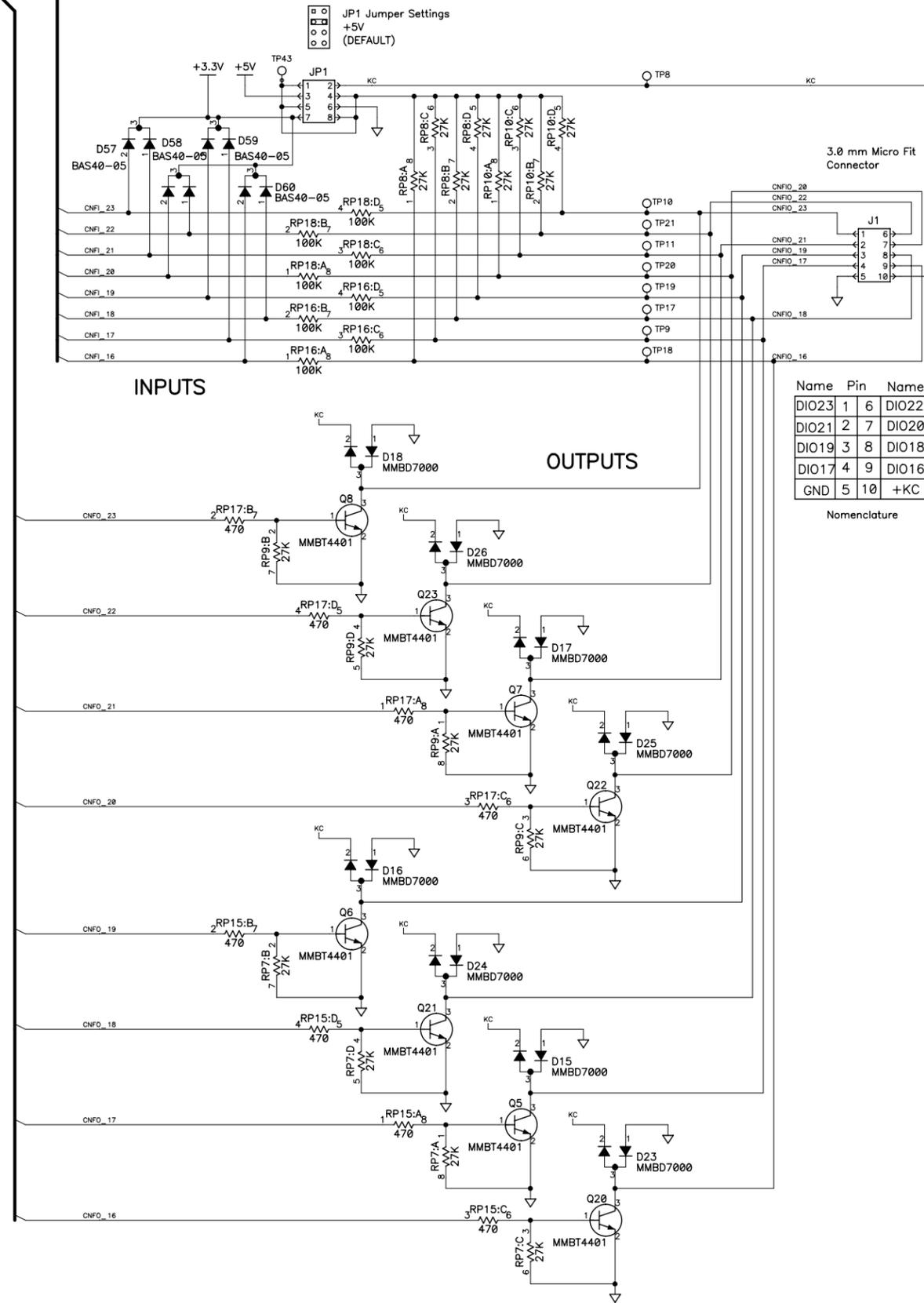
INPUTS

OUTPUTS

Name	Pin	Name
DIO31	1	DIO30
DIO29	2	DIO28
DIO27	3	DIO26
DIO25	4	DIO24
GND	5	+KD

Nomenclature

CONFIGURABLE I/O



INPUTS

OUTPUTS

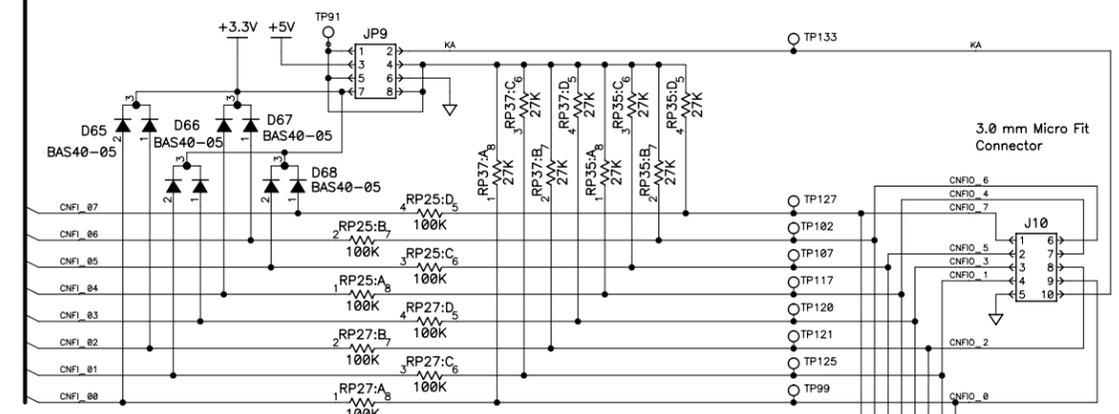
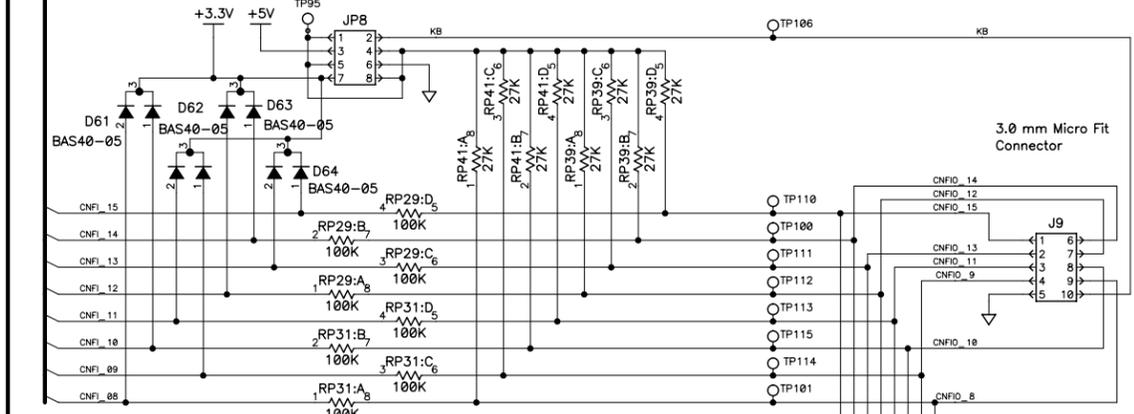
Name	Pin	Name
DIO23	1	DIO22
DIO21	2	DIO20
DIO19	3	DIO18
DIO17	4	DIO16
GND	5	+KC

Nomenclature

CONFIGURABLE I/O

JP8 Jumper Settings
+5V
(DEFAULT)

JP9 Jumper Settings
+5V
(DEFAULT)



INPUTS

INPUTS

OUTPUTS

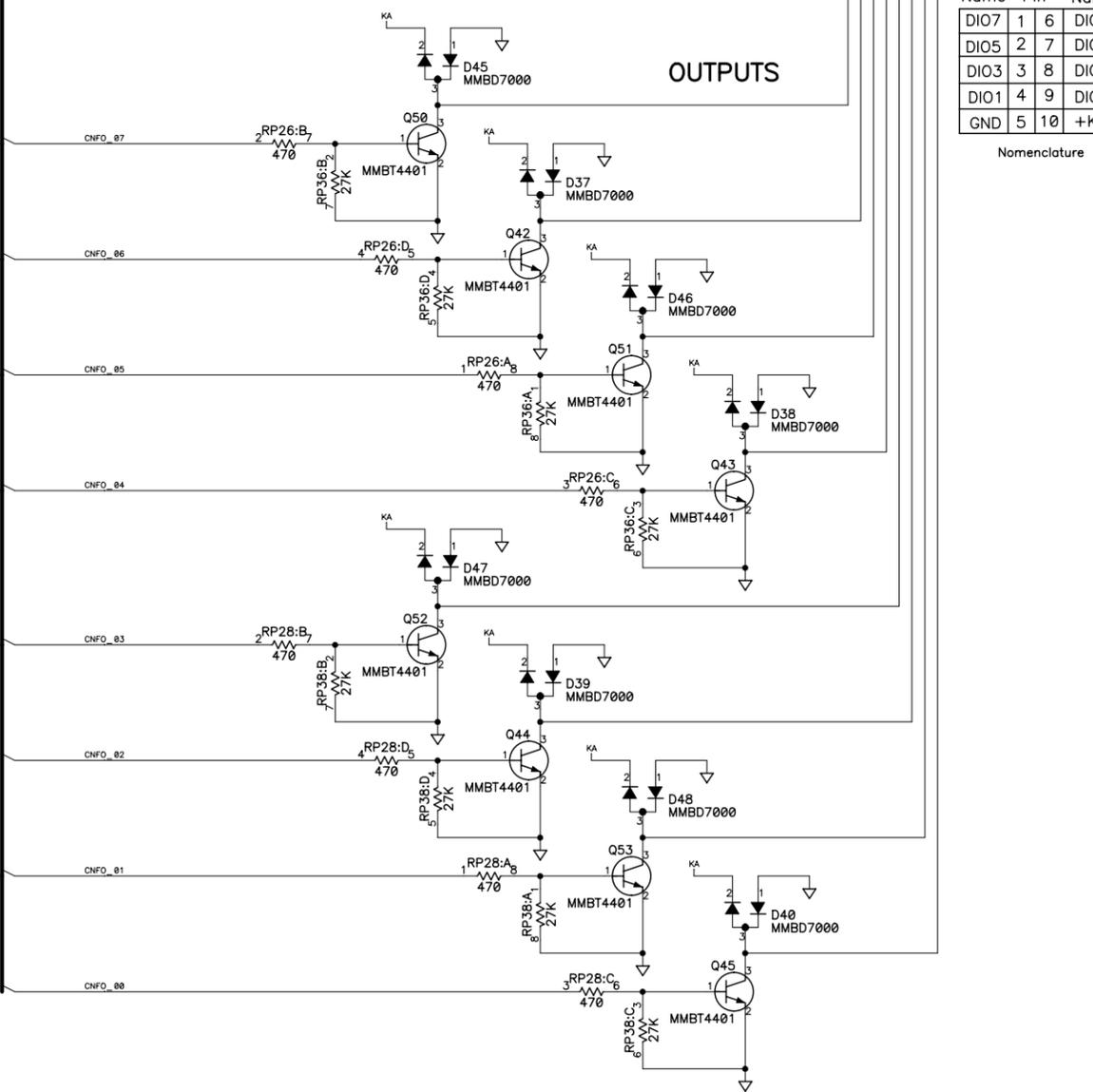
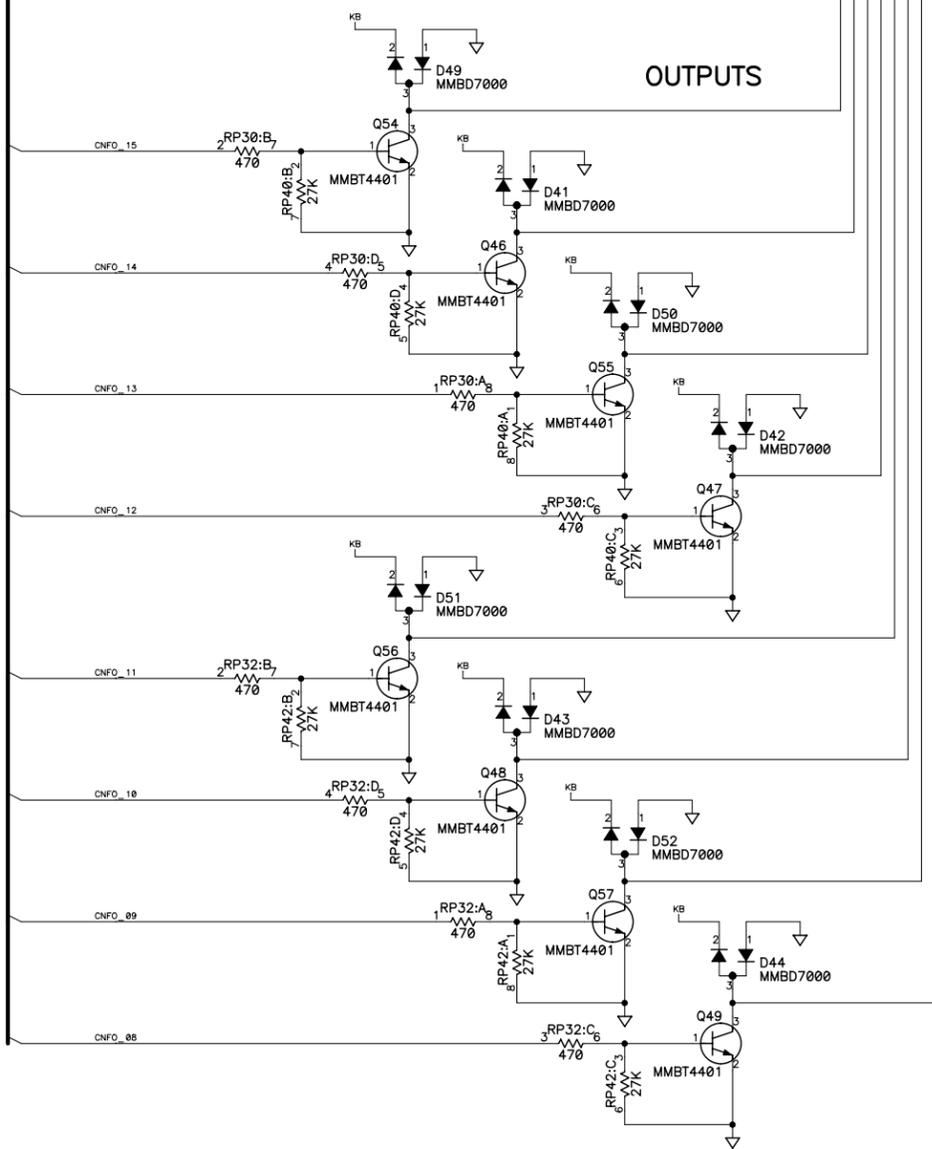
OUTPUTS

Name	Pin	Name
DIO15	1	DIO14
DIO13	2	DIO12
DIO11	3	DIO10
DIO9	4	DIO8
GND	5	+KB

Name	Pin	Name
DIO7	1	DIO6
DIO5	2	DIO4
DIO3	3	DIO2
DIO1	4	DIO0
GND	5	+KA

Nomenclature

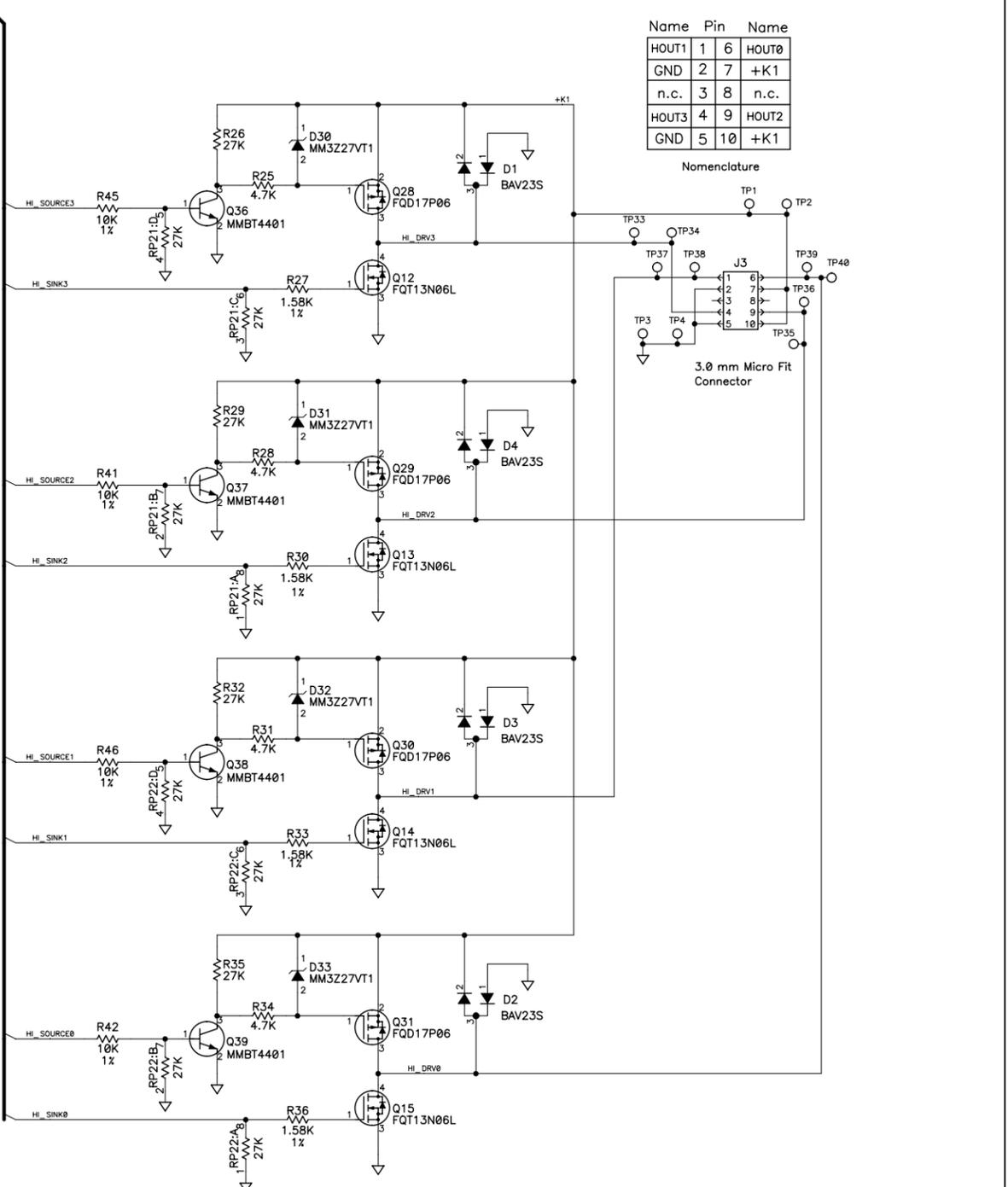
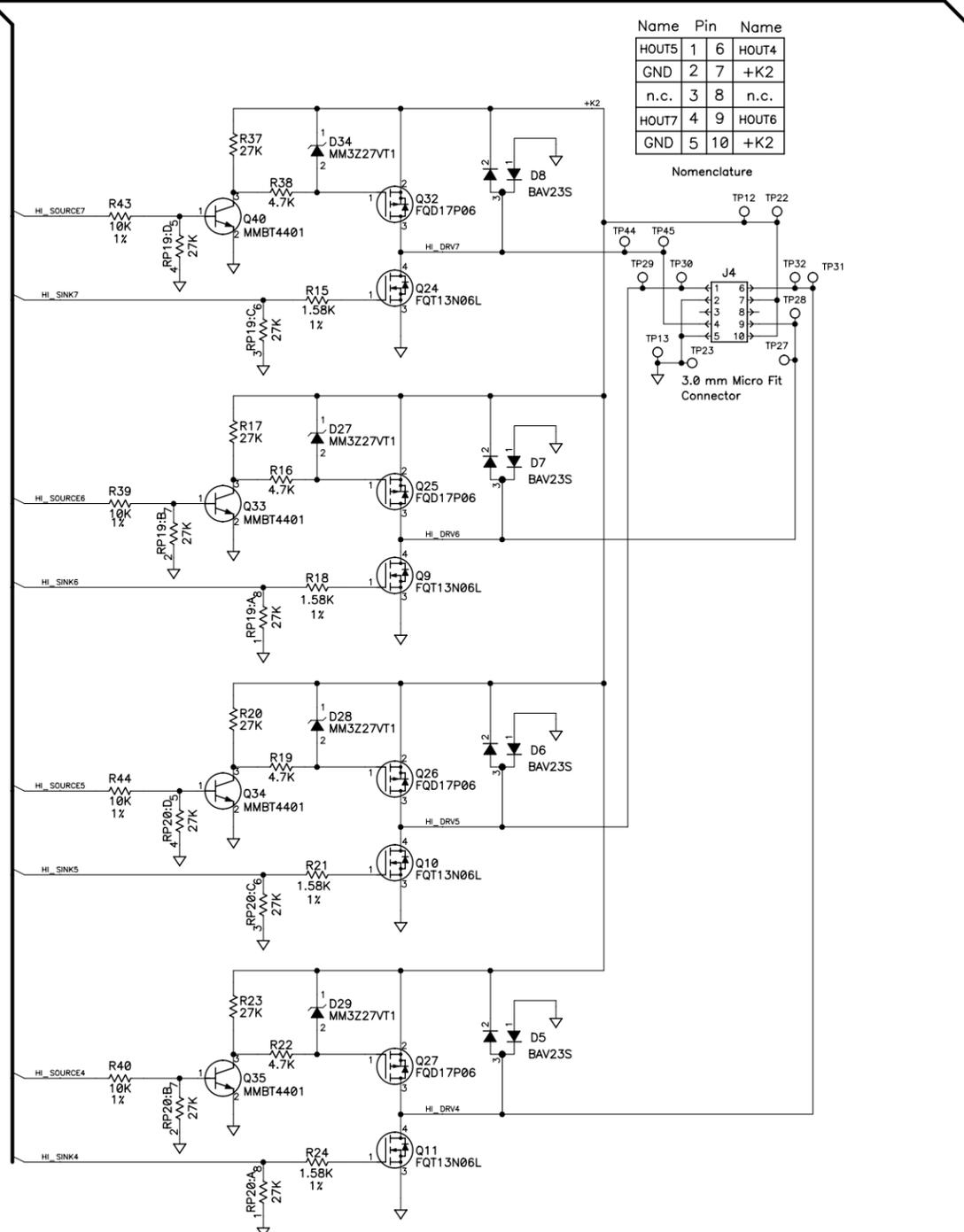
Nomenclature

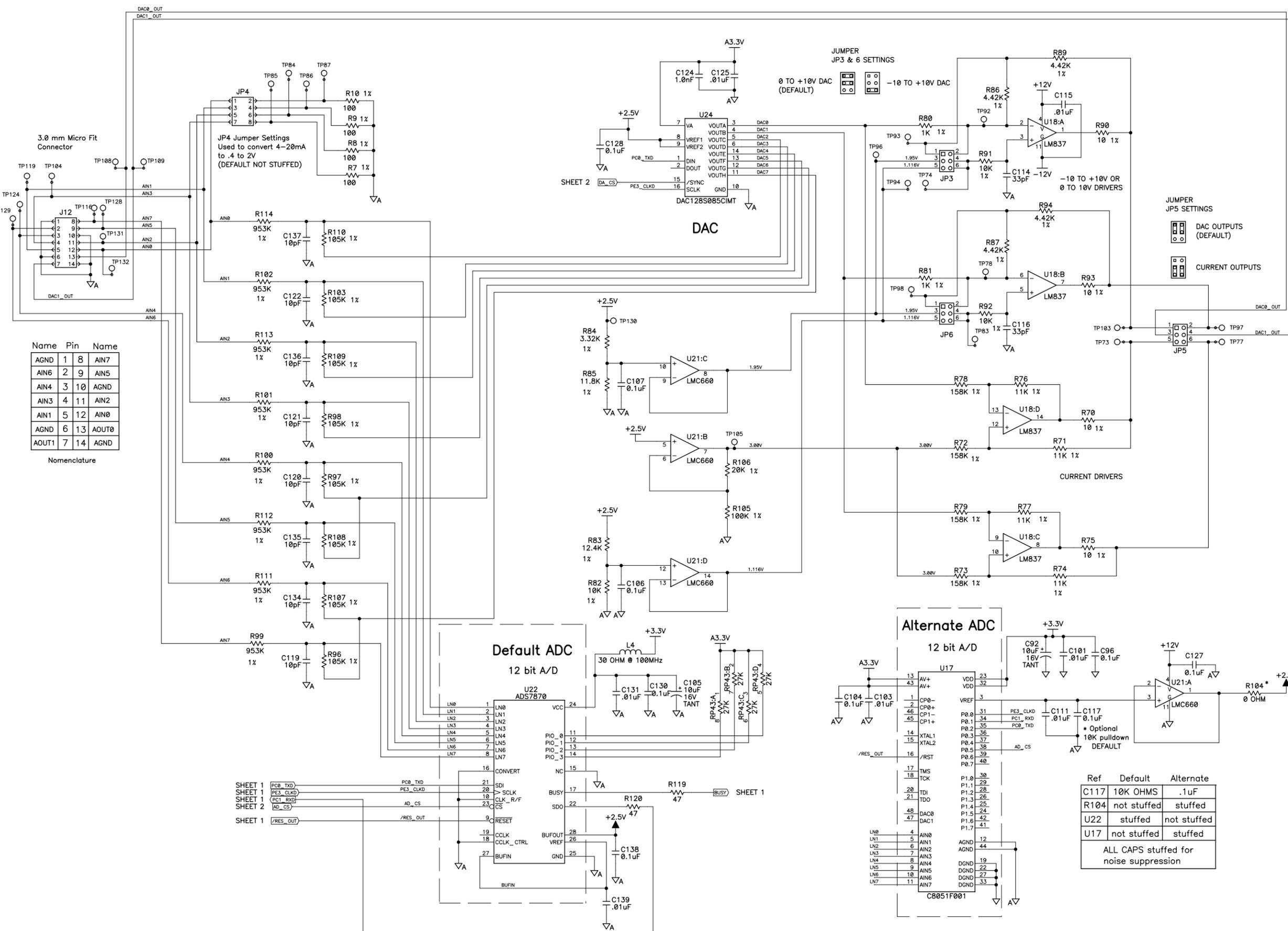


HIGH CURRENT OUTPUTS

HIGH CURRENT OUTPUTS

SHEET 2

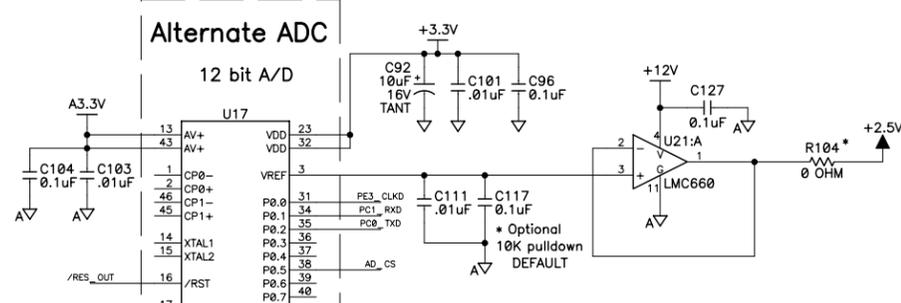
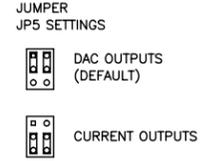
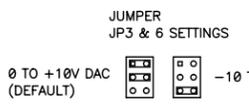




Name	Pin	Name	
AGND	1	8	AIN7
AIN6	2	9	AIN5
AIN4	3	10	AGND
AIN3	4	11	AIN2
AIN1	5	12	AIN0
AGND	6	13	AOUT0
AOUT1	7	14	AGND

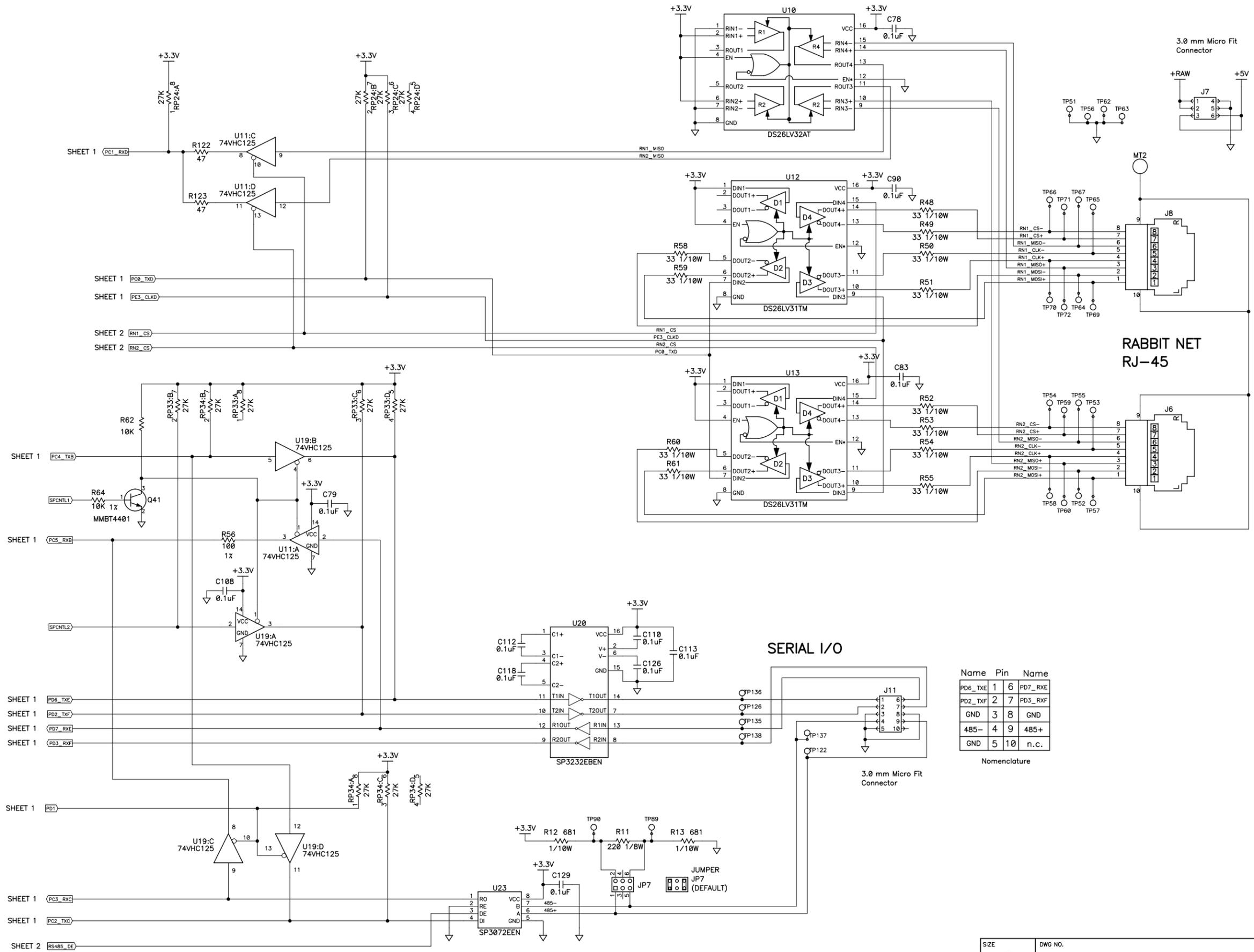
Nomenclature

SHEET	PCB_TXD	PCB_TXD	PCB_TXD	PCB_TXD
SHEET 1	PCB_TXD	PCB_TXD	PCB_TXD	PCB_TXD
SHEET 1	PE3_CLKD	PE3_CLKD	PE3_CLKD	PE3_CLKD
SHEET 1	PC1_RXD	PC1_RXD	PC1_RXD	PC1_RXD
SHEET 2	AD_CS	AD_CS	AD_CS	AD_CS
SHEET 1	/RES_OUT	/RES_OUT	/RES_OUT	/RES_OUT



Ref	Default	Alternate
C117	10K OHMS	.1uF
R104	not stuffed	stuffed
U22	stuffed	not stuffed
U17	not stuffed	stuffed

ALL CAPS stuffed for noise suppression



**RABBIT NET
RJ-45**

SERIAL I/O

Name	Pin	Name
PD6_TXE	1	PD7_RXE
PD2_TXF	2	PD3_RXF
GND	3	GND
485-	4	485+
GND	5	n.c.

Nomenclature