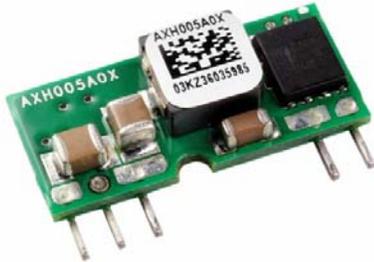


## Austin MicroLynx™ SIP Non-isolated Power Modules: 3Vdc – 5.5Vdc input; 0.75Vdc to 3.63Vdc Output; 5A Output Current

### RoHS Compliant



### Applications

- Distributed power architectures
- Intermediate bus voltage applications
- Telecommunications equipment
- Servers and storage applications
- Networking equipment
- Enterprise Networks
- Latest generation IC's (DSP, FPGA, ASIC) and Microprocessor powered applications

### Description

Austin MicroLynx™ SIP (single in-line package) power modules are non-isolated dc-dc converters that can deliver up to 5A of output current with full load efficiency of 94% at 3.63V output. These modules provide precisely regulated output voltage programmable via external resistor from 0.75Vdc to 3.63Vdc over a wide range of input voltage ( $V_{IN} = 3.0 - 5.5V$ ). Their open-frame construction and small footprint enable designers to develop cost- and space-efficient solutions. Standard features include remote On/Off, programmable output voltage and overcurrent protection.

### Features

- Compliant to RoHS EU Directive 2002/95/EC (-Z versions)
- Compliant to ROHS EU Directive 2002/95/EC with lead solder exemption (non-Z versions)
- Delivers up to 5A output current
- High efficiency – 94% at 3.3V full load ( $V_{IN} = 5.0V$ )
- Small size and low profile:  
22.9 mm x 10.2 mm x 6.66 mm  
(0.9 in x 0.4 in x 0.262 in)
- Low output ripple and noise
- High Reliability:  
Calculated MTBF = 19M hours at 25°C Full-load
- Output voltage programmable from 0.75 Vdc to 3.63Vdc via external resistor
- Line Regulation: 0.3% (typical)
- Load Regulation: 0.4% (typical)
- Temperature Regulation: 0.4 % (typical)
- Remote On/Off
- Output overcurrent protection (non-latching)
- Wide operating temperature range (-40°C to 85°C)
- UL\* 60950-1 Recognized, CSA† C22.2 No. 60950-1-03 Certified, and VDE‡ 0805:2001-12 (EN60950-1) Licensed
- ISO\*\* 9001 and ISO 14001 certified manufacturing facilities

\* UL is a registered trademark of Underwriters Laboratories, Inc.

† CSA is a registered trademark of Canadian Standards Association.

‡ VDE is a trademark of Verband Deutscher Elektrotechniker e.V.

\*\* ISO is a registered trademark of the International Organization of Standards

## Absolute Maximum Ratings

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only, functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect the device reliability.

Parameter	Device	Symbol	Min	Max	Unit
Input Voltage Continuous	All	$V_{IN}$	-0.3	5.8	Vdc
Operating Ambient Temperature (see Thermal Considerations section)	All	$T_A$	-40	85	°C
Storage Temperature	All	$T_{stg}$	-55	125	°C

## Electrical Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

Parameter	Device	Symbol	Min	Typ	Max	Unit
Operating Input Voltage	All	$V_{IN}$	3.0	-	5.5	Vdc
Maximum Input Current ( $V_{IN} = V_{IN, min}$ to $V_{IN, max}$ , $I_O = I_{O, max}$ )	All	$I_{IN, max}$			5.0	Adc
Input No Load Current ( $V_{IN} = V_{IN, nom}$ , $I_O = 0$ , module enabled)	$V_{O, set} = 0.75$ Vdc $V_{O, set} = 3.3$ Vdc	$I_{IN, No load}$ $I_{IN, No load}$		20 45		mA mA
Input Stand-by Current ( $V_{IN} = V_{IN, nom}$ , module disabled)	All	$I_{IN, stand-by}$		0.6		mA
Inrush Transient	All	$I^2t$			0.04	A <sup>2</sup> s
Input Reflected Ripple Current, peak-to-peak (5Hz to 20MHz, 1μH source impedance; $V_{IN, min}$ to $V_{IN, max}$ , $I_O = I_{O, max}$ ; See Test configuration section)	All			35		mAp-p
Input Ripple Rejection (120Hz)	All			30		dB

### CAUTION: This power module is not internally fused. An input line fuse must always be used.

This power module can be used in a wide variety of applications, ranging from simple standalone operation to being part of a complex power architecture. To preserve maximum flexibility, internal fusing is not included, however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a fast-acting fuse with a maximum rating of 6 A (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data sheet for further information.

Electrical Specifications (continued)

Parameter	Device	Symbol	Min	Typ	Max	Unit
Output Voltage Set-point ( $V_{IN}=V_{IN, min}$ , $I_O=I_{O, max}$ , $T_A=25^\circ C$ )	All	$V_{O, set}$	-2.0	$V_{O, set}$	+2.0	% $V_{O, set}$
Output Voltage (Over all operating input voltage, resistive load, and temperature conditions until end of life)	All	$V_{O, set}$	-3%	—	+3.0%	% $V_{O, set}$
Adjustment Range Selected by an external resistor	All	$V_O$	0.7525		3.63	Vdc
Output Regulation Line ( $V_{IN}=V_{IN, min}$ to $V_{IN, max}$ ) Load ( $I_O=I_{O, min}$ to $I_{O, max}$ ) Temperature ( $T_{ref}=T_{A, min}$ to $T_{A, max}$ )	All All All		— — —	0.3 0.4 0.4	— — —	% $V_{O, set}$ % $V_{O, set}$ % $V_{O, set}$
Output Ripple and Noise on nominal output ( $V_{IN}=V_{IN, nom}$ and $I_O=I_{O, min}$ to $I_{O, max}$ $C_{out} = 1\mu F$ ceramic//10 $\mu F$ tantalum capacitors)						
RMS (5Hz to 20MHz bandwidth)	All		—	10	15	mV <sub>rms</sub>
Peak-to-Peak (5Hz to 20MHz bandwidth)	All		—	40	50	mV <sub>pk-pk</sub>
External Capacitance ESR $\geq 1$ m $\Omega$ ESR $\geq 10$ m $\Omega$	All All	$C_{O, max}$ $C_{O, max}$	— —	— —	1000 3000	$\mu F$ $\mu F$
Output Current	All	$I_o$	0		5	Adc
Output Current Limit Inception (Hiccup Mode ) ( $V_O = 90\%$ of $V_{O, set}$ )	All	$I_{o, lim}$	—	220	—	% $I_o$
Output Short-Circuit Current ( $V_O \leq 250mV$ ) ( Hiccup Mode )	All	$I_{o, s/c}$	—	2	—	Adc
Efficiency $V_{IN} = V_{IN, nom}$ , $T_A = 25^\circ C$ $I_O = I_{O, max}$ , $V_O = V_{O, set}$	$V_{O, set} = 0.75Vdc$ $V_{O, set} = 1.2Vdc$ $V_{O, set} = 1.5Vdc$ $V_{O, set} = 1.8Vdc$ $V_{O, set} = 2.5Vdc$ $V_{O, set} = 3.3Vdc$	$\eta$ $\eta$ $\eta$ $\eta$ $\eta$ $\eta$		79.0 85.0 87.0 88.5 92.0 94.0		% % % % % %
Switching Frequency	All	$f_{sw}$	—	300	—	kHz
Dynamic Load Response ( $dI_O/dt=2.5A/\mu s$ ; $V_{IN} = V_{IN, nom}$ ; $T_A=25^\circ C$ ) Load Change from $I_O = 50\%$ to $100\%$ of $I_{O, max}$ ; $1\mu F$ ceramic// $10\mu F$ tantalum Peak Deviation Settling Time ( $V_O < 10\%$ peak deviation)	All All	$V_{pk}$ $t_s$	— —	130 25	— —	mV $\mu s$
( $dI_O/dt=2.5A/\mu s$ ; $V_{IN} = V_{IN, nom}$ ; $T_A=25^\circ C$ ) Load Change from $I_O = 100\%$ to $50\%$ of $I_{O, max}$ : $1\mu F$ ceramic// $10\mu F$ tantalum Peak Deviation Settling Time ( $V_O < 10\%$ peak deviation)	All	$V_{pk}$ $t_s$	— —	130 25	— —	mV $\mu s$

### Electrical Specifications (continued)

Parameter	Device	Symbol	Min	Typ	Max	Unit
Dynamic Load Response ( $dI_o/dt=2.5A/\mu s$ ; $V_{IN} = V_{IN, nom}$ ; $T_A=25^\circ C$ ) Load Change from $I_o= 50\%$ to $100\%$ of $I_{o,max}$ ; $C_o = 2 \times 150 \mu F$ polymer capacitors Peak Deviation	All	$V_{pk}$	—	50	—	mV
Settling Time ( $V_o < 10\%$ peak deviation)	All	$t_s$	—	50	—	$\mu s$
( $dI_o/dt=2.5A/\mu s$ ; $V_{IN} = V_{IN, nom}$ ; $T_A=25^\circ C$ ) Load Change from $I_o= 100\%$ to $50\%$ of $I_{o,max}$ ; $C_o = 2 \times 150 \mu F$ polymer capacitors Peak Deviation	All	$V_{pk}$	—	50	—	mV
Settling Time ( $V_o < 10\%$ peak deviation)	All	$t_s$	—	50	—	$\mu s$

### General Specifications

Parameter	Min	Typ	Max	Unit
Calculated MTBF ( $I_o=I_{o, max}$ , $T_A=25^\circ C$ )		19,000,000		Hours
Weight	—	2.8 (0.1)	—	g (oz.)

## Feature Specifications

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for additional information.

Parameter	Device	Symbol	Min	Typ	Max	Unit
Remote On/Off Signal interface ( $V_{IN}=V_{IN, min}$ to $V_{IN, max}$ ; Open collector pnp or equivalent Compatible, Von/off signal referenced to GND See feature description section) Logic Low (On/Off Voltage pin open - Module ON)						
Von/Off	All	$V_{IL}$	—	—	0.4	V
Ion/Off	All	$I_{IL}$	—	—	10	$\mu A$
Logic High (Von/Off > 2.5V – Module Off)						
Von/Off	All	$V_{IH}$	—	—	$V_{IN, max}$	V
Ion/off	All	$I_{IH}$	—	—	1	mA
Turn-On Delay and Rise Times ( $I_O=I_{O, max}$ , $V_{IN} = V_{IN, nom}$ , $T_A = 25^\circ C$ , )						
Case 1: On/Off input is set to Logic Low (Module ON) and then input power is applied (delay from instant at which $V_{IN} = V_{IN, min}$ until $V_O=10\%$ of $V_{O, set}$ )	All	Tdelay	—	3.9	—	msec
Case 2: Input power is applied for at least one second and then the On/Off input is set to logic Low (delay from instant at which Von/Off=0.3V until $V_O=10\%$ of $V_{O, set}$ )	All	Tdelay	—	3.9	—	msec
Output voltage Rise time (time for $V_O$ to rise from 10% of $V_{O, set}$ to 90% of $V_{O, set}$ )	All	Trise	—	4.2	8.5	msec
Output voltage overshoot – Startup $I_O= I_{O, max}$ ; $V_{IN} = 3.0$ to 5.5Vdc, $T_A = 25^\circ C$				—	1	% $V_{O, set}$
Overtemperature Protection (See Thermal Consideration section)	All	$T_{ref}$	—	150	—	$^\circ C$
Input Undervoltage Lockout						
Turn-on Threshold	All			2.2		V
Turn-off Threshold	All			2.0		V

### Characteristic Curves

The following figures provide typical characteristics for the Austin MicroLynx™ SIP modules at 25°C.

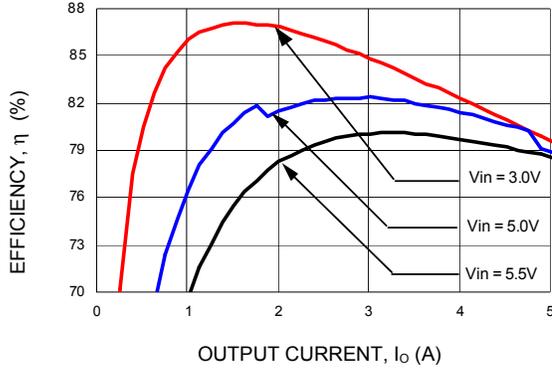


Figure 1. Converter Efficiency versus Output Current ( $V_{out} = 0.75V_{dc}$ ).

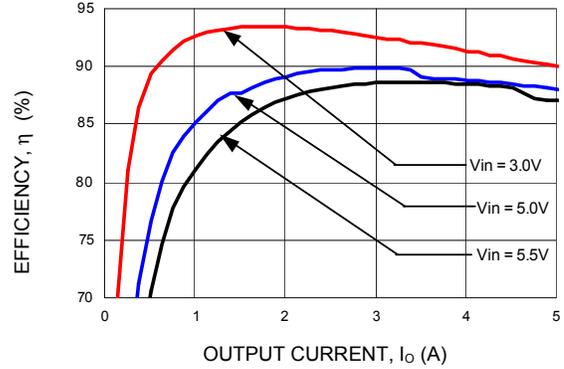


Figure 4. Converter Efficiency versus Output Current ( $V_{out} = 1.8V_{dc}$ ).

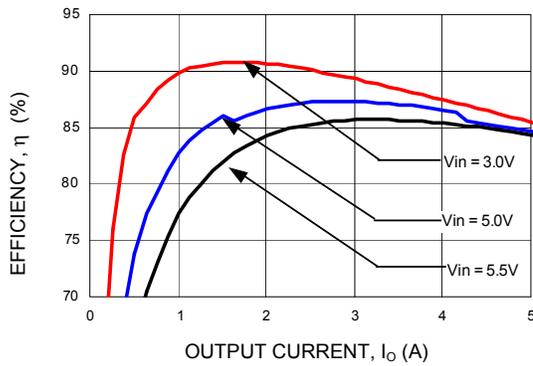


Figure 2. Converter Efficiency versus Output Current ( $V_{out} = 1.2V_{dc}$ ).

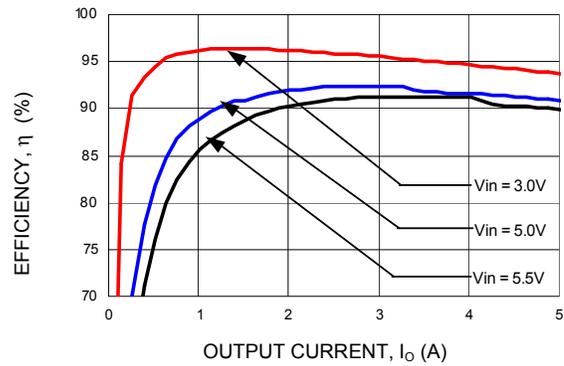


Figure 5. Converter Efficiency versus Output Current ( $V_{out} = 2.5V_{dc}$ ).

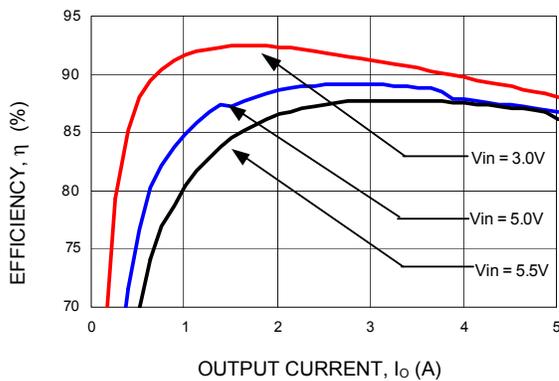


Figure 3. Converter Efficiency versus Output Current ( $V_{out} = 1.5V_{dc}$ ).

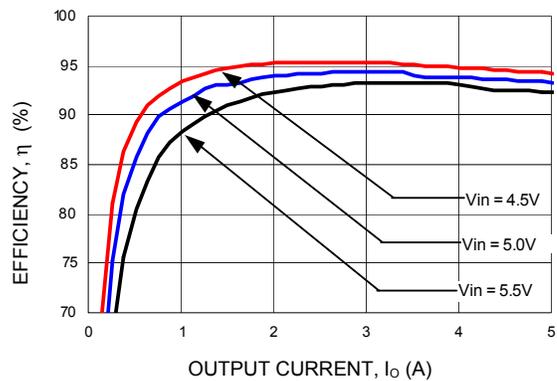


Figure 6. Converter Efficiency versus Output Current ( $V_{out} = 3.3V_{dc}$ ).

Characteristic Curves (continued)

The following figures provide typical characteristics for the MicroLynx™ SIP modules at 25°C.

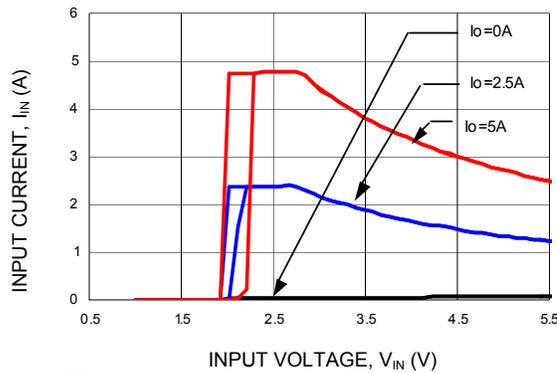


Figure 7. Input voltage vs. Input Current  
(V<sub>out</sub> = 2.5Vdc).

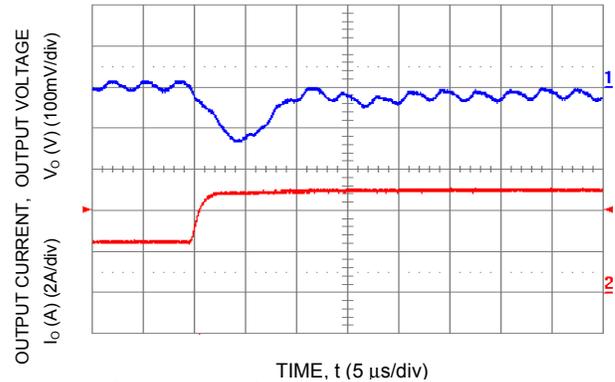


Figure 10. Transient Response to Dynamic Load Change from 50% to 100% of full load (V<sub>o</sub> = 3.3Vdc).

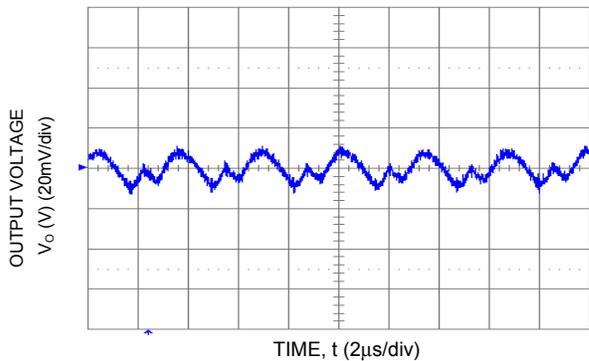


Figure 8. Typical Output Ripple and Noise  
(V<sub>in</sub> = 5V dc, V<sub>o</sub> = 0.75 Vdc, I<sub>o</sub>=5A).

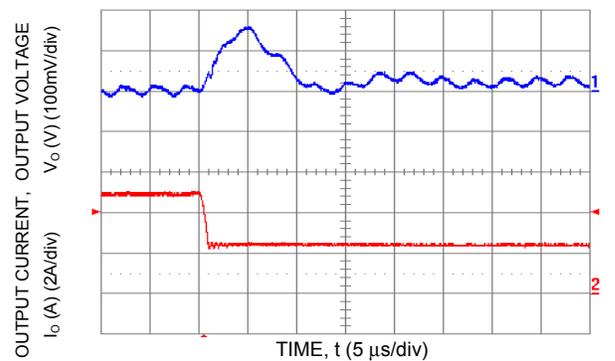


Figure 11. Transient Response to Dynamic Load Change from 100% to 50% of full load (V<sub>o</sub> = 3.3 Vdc).

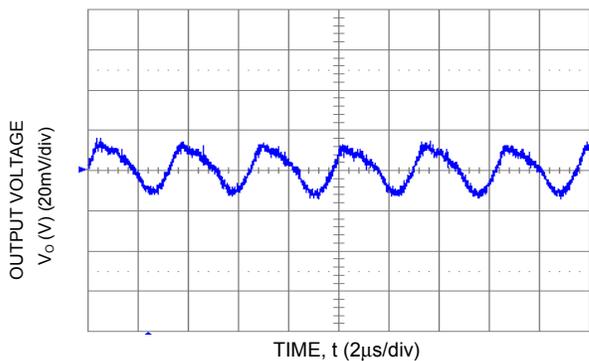


Figure 9. Typical Output Ripple and Noise  
(V<sub>in</sub> = 5V dc, V<sub>o</sub> = 3.3 Vdc, I<sub>o</sub>=5A).

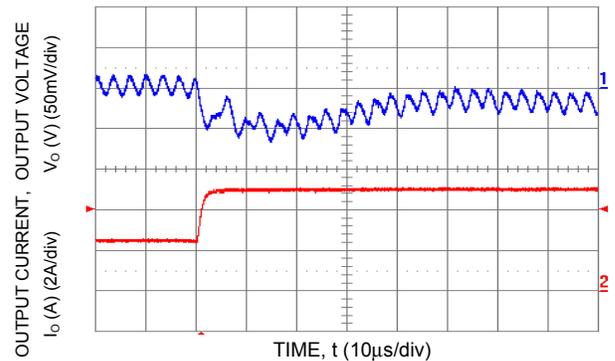


Figure 12. Transient Response to Dynamic Load Change from 50% to 100% of full load (V<sub>o</sub> = 3.3 Vdc, C<sub>ext</sub> = 2x150 μF Polymer Capacitors).

Characteristic Curves (continued)

The following figures provide typical characteristics for the Austin MicroLynx™ SIP modules at 25°C.

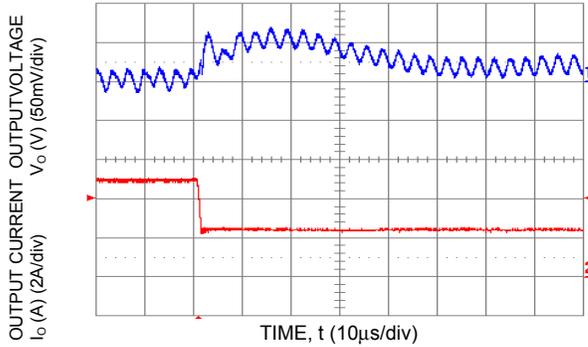


Figure 13. Transient Response to Dynamic Load Change from 100% of 50% full load ( $V_o = 3.3\text{ Vdc}$ ,  $C_{ext} = 2 \times 150\ \mu\text{F}$  Polymer Capacitors).

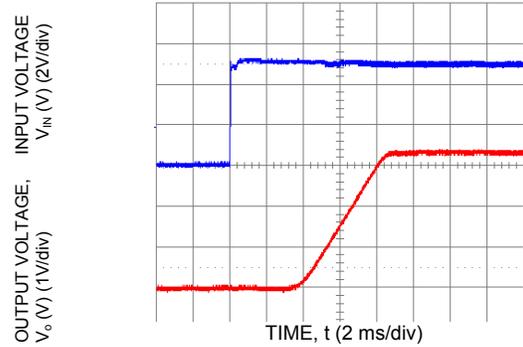


Figure 16. Typical Start-Up with application of  $V_{in}$  with ( $V_{in} = 5.0\text{Vdc}$ ,  $V_o = 3.3\text{Vdc}$ ,  $I_o = 5\text{A}$ ).

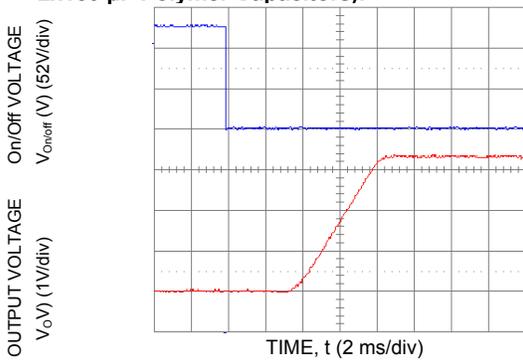


Figure 14. Typical Start-Up Using Remote On/Off ( $V_{in} = 5\text{Vdc}$ ,  $V_o = 3.3\text{Vdc}$ ,  $I_o = 5.0\text{A}$ ).

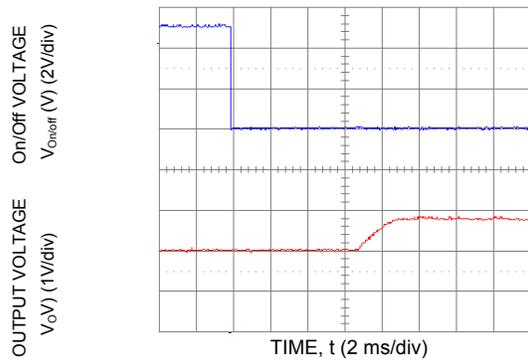


Figure 17 Typical Start-Up using Remote On/off with Prebias ( $V_{in} = 3.3\text{Vdc}$ ,  $V_o = 1.8\text{Vdc}$ ,  $I_o = 1\text{A}$ ,  $V_{bias} = 1.0\text{Vdc}$ ).

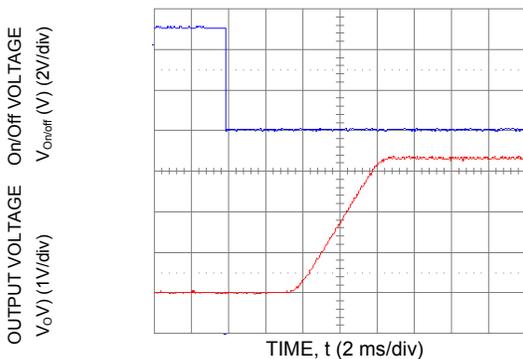


Figure 15. Typical Start-Up Using Remote On/Off with Low-ESR external capacitors ( $7 \times 150\ \mu\text{F}$  Polymer) ( $V_{in} = 5\text{Vdc}$ ,  $V_o = 3.3\text{Vdc}$ ,  $I_o = 5.0\text{A}$ ,  $C_o = 1050\ \mu\text{F}$ ).

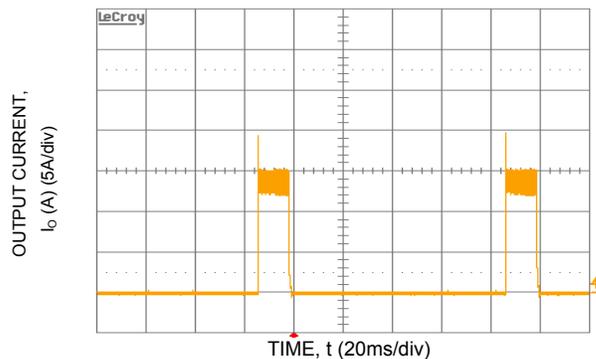


Figure 18. Output short circuit Current ( $V_{in} = 5\text{Vdc}$ ,  $V_o = 0.75\text{Vdc}$ ).

Characteristic Curves (continued)

The following figures provide thermal derating curves for the Austin MicroLynx™ SIP modules.

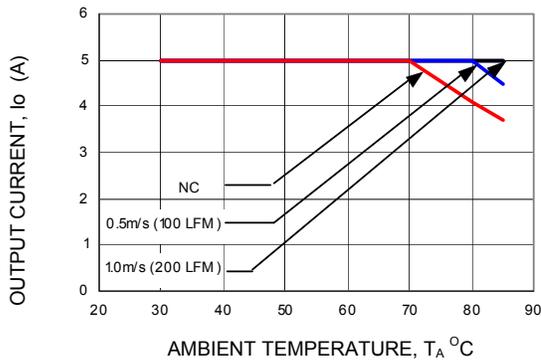


Figure 19. Derating Output Current versus Local Ambient Temperature and Airflow ( $V_{in} = 5.0Vdc$ ,  $V_o=3.3Vdc$ ).

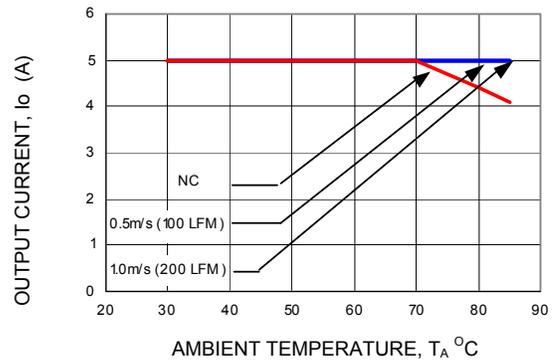


Figure 22. Derating Output Current versus Local Ambient Temperature and Airflow ( $V_{in} = 3.3Vdc$ ,  $V_o=0.75 Vdc$ ).

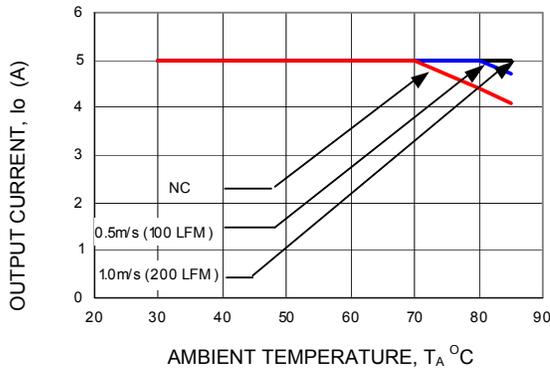


Figure 20. Derating Output Current versus Local Ambient Temperature and Airflow ( $V_{in} = 5.0Vdc$ ,  $V_o=0.75 Vdc$ ).

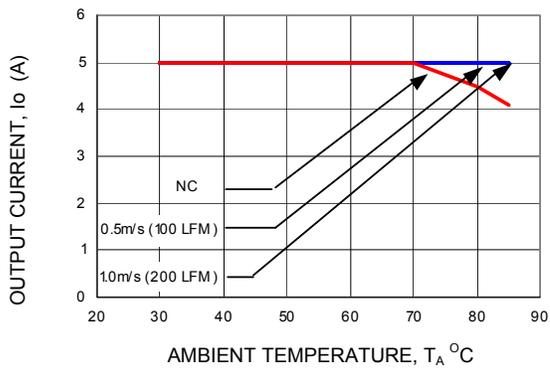
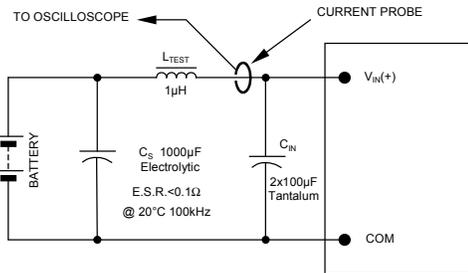


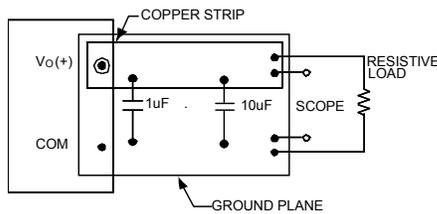
Figure 21. Derating Output Current versus Local Ambient Temperature and Airflow ( $V_{in} = 3.3Vdc$ ,  $V_o=2.5 Vdc$ ).

## Test Configurations



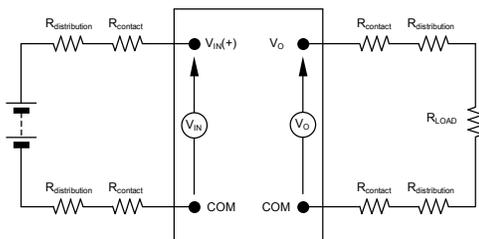
NOTE: Measure input reflected ripple current with a simulated source inductance ( $L_{TEST}$ ) of 1µH. Capacitor  $C_S$  offsets possible battery impedance. Measure current as shown above.

Figure 23. Input Reflected Ripple Current Test Setup.



NOTE: All voltage measurements to be taken at the module terminals, as shown above. If sockets are used then Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.

Figure 24. Output Ripple and Noise Test Setup.



NOTE: All voltage measurements to be taken at the module terminals, as shown above. If sockets are used then Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.

Figure 25. Output Voltage and Efficiency Test Setup.

$$\text{Efficiency } \eta = \frac{V_O \cdot I_O}{V_{IN} \cdot I_{IN}} \times 100 \%$$

## Design Considerations

### Input Filtering

The Austin MicroLynx™ SIP module should be connected to a low-impedance source. A highly inductive source can affect the stability of the module. An input capacitance must be placed directly adjacent to the input pin of the module, to minimize input ripple voltage and ensure module stability.

To minimize input voltage ripple, low-ESR polymer and ceramic capacitors are recommended at the input of the module. Figure 26 shows the input ripple voltage (mVp-p) for various outputs with 1x150 µF polymer capacitors (Panasonic p/n: EEFUE0J151R, Sanyo p/n: 6TPE150M) in parallel with 1 x 47 µF ceramic capacitor (Panasonic p/n: ECJ-5YB0J476M, Taiyo- Yuden p/n: CEJMK432BJ476MMT) at full load. Figure 27 shows the input ripple with 2x150 µF polymer capacitors in parallel with 2 x 47 µF ceramic capacitor at full load.

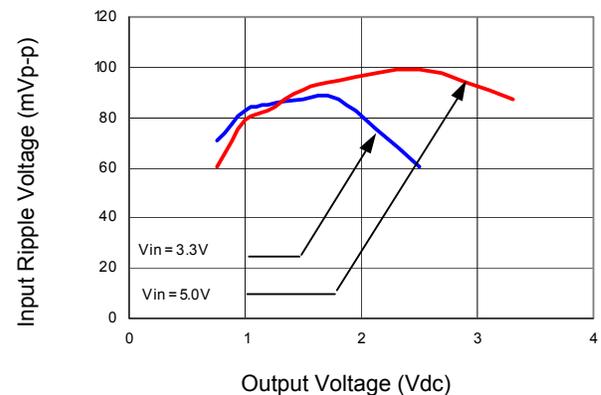


Figure 26. Input ripple voltage for various output with 1x150 µF polymer and 1x47 µF ceramic capacitors at the input (full load)

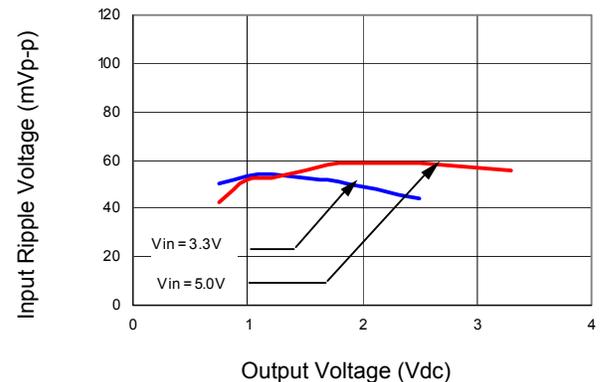


Figure 27. Input ripple voltage for various output with 2x150 µF polymer and 2x47 µF ceramic capacitors at the input (full load)

## Design Considerations (continued)

### Output Filtering

The Austin MicroLynx™ SIP module is designed for low output ripple voltage and will meet the maximum output ripple specification with 1  $\mu$ F ceramic and 10  $\mu$ F polymer capacitors at the output of the module. However, additional output filtering may be required by the system designer for a number of reasons. First, there may be a need to further reduce the output ripple and noise of the module. Second, the dynamic response characteristics may need to be customized to a particular load step change.

To reduce the output ripple and improve the dynamic response to a step load change, additional capacitance at the output can be used. Low ESR polymer and ceramic capacitors are recommended to improve the dynamic response of the module. For stable operation of the module, limit the capacitance to less than the maximum output capacitance as specified in the electrical specification table.

## Safety Considerations

For safety agency approval the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standards, i.e., UL 60950-1, CSA C22.2 No. 60950-1-03, and VDE 0850:2001-12 (EN60950-1) Licensed.

For the converter output to be considered meeting the requirements of safety extra-low voltage (SELV), the input must meet SELV requirements. The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

The input to these units is to be provided with a fast-acting fuse with a maximum rating of 6A in the positive input lead.



## Feature Descriptions (continued)

### Output Voltage Programming

The output voltage of the Austin MicroLynx™ can be programmed to any voltage from 0.75Vdc to 3.63Vdc by connecting a resistor (shown as  $R_{trim}$  in Figure 29) between Trim and GND pins of the module. Without an external resistor between Trim and GND pins, the output of the module will be 0.7525Vdc. To calculate the value of the trim resistor,  $R_{trim}$  for a desired output voltage, use the following equation:

$$R_{trim} = \left[ \frac{21070}{V_o - 0.7525} - 5110 \right] \Omega$$

$R_{trim}$  is the external resistor in  $\Omega$

$V_o$  is the desired output voltage

For example, to program the output voltage of the Austin MicroLynx™ module to 1.8V,  $R_{trim}$  is calculated as follows:

$$R_{trim} = \left[ \frac{21070}{1.8 - 0.7525} - 5110 \right]$$

$$R_{trim} = 9.024k\Omega$$

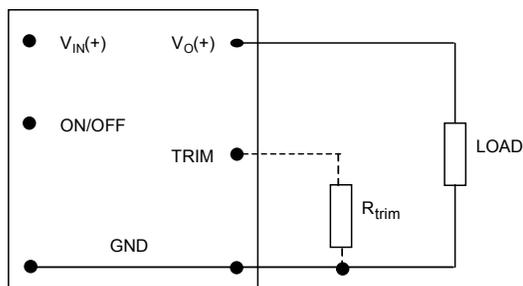


Figure 29. Circuit configuration to program output voltage using an external resistor

Austin MicroLynx™ can also be programmed by applying a voltage between TRIM and GND pins (Figure 30). The following equation can be used to determine the value of  $V_{trim}$  needed to obtain a desired output voltage  $V_o$ :

$$V_{trim} = (0.7 - 0.1698 \times \{V_o - 0.7525\})$$

For example, to program the output voltage of a MicroLynx™ module to 3.3 Vdc,  $V_{trim}$  is calculated as follows:

$$V_{trim} = (0.7 - 0.1698 \times \{3.3 - 0.7525\})$$

$$V_{trim} = 0.2670V$$

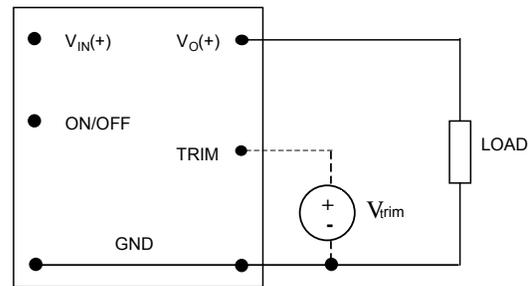


Figure 30. Circuit Configuration for programming Output voltage using external voltage source

Table 1 provides  $R_{trim}$  values for most common output voltages. Table 2 provides values of external voltage source,  $V_{trim}$  for various output voltage.

Table 1

$V_{O, set}$ (V)	$R_{trim}$ (K $\Omega$ )
0.7525	Open
1.2	41.973
1.5	23.077
1.8	15.004
2.5	6.947
3.3	3.160

Table 2

$V_{O, set}$ (V)	$V_{trim}$ (V)
0.7525	Open
1.2	0.6240
1.5	0.5731
1.8	0.5221
2.5	0.4033
3.3	0.2670

Using 1% tolerance trim resistor, set point tolerance of  $\pm 2\%$  is achieved as specified in the electrical specification. The POL Programming Tool, available at [www.lineagepower.com](http://www.lineagepower.com) under the Design Tools section, helps determine the required external trim resistor needed for a specific output voltage.

## Feature Descriptions (continued)

The amount of power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. When using the trim feature, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power ( $P_{max} = V_{o,set} \times I_{o,max}$ ).

## Voltage Margining

Output voltage margining can be implemented in the Austin MicroLynx™ modules by connecting a resistor,  $R_{margin-up}$ , from Trim pin to ground pin for margining-up the output voltage and by connecting a resistor,  $R_{margin-down}$ , from Trim pin to Output pin. Figure 31 shows the circuit configuration for output voltage margining. The POL Programming Tool, available at [www.lineagepower.com](http://www.lineagepower.com) under the Design Tools section, also calculates the values of  $R_{margin-up}$  and  $R_{margin-down}$  for a specific output voltage and % margin. Please consult your local Lineage Power technical representative for additional details.

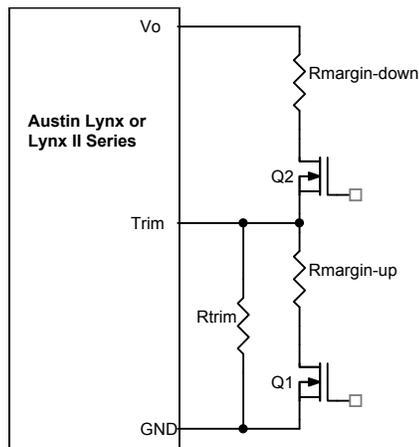
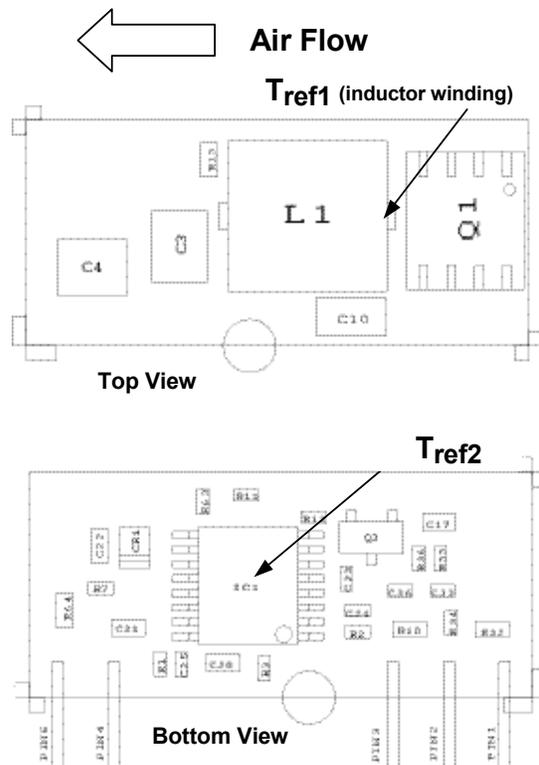


Figure 31. Circuit Configuration for margining Output voltage.

## Thermal Considerations

Power modules operate in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation.

Considerations include ambient temperature, airflow, module power dissipation, and the need for increased reliability. A reduction in the operating temperature of the module will result in an increase in reliability. The thermal data presented here is based on physical measurements taken in a wind tunnel. The test set-up is shown in Figure 33. Note that the airflow is parallel to the long axis of the module as shown in figure 32. The derating data applies to airflow in either direction of the module's long axis.



## Post solder Cleaning and Drying Considerations

Post solder cleaning is usually the final circuit-board assembly process prior to electrical board testing. The result of inadequate cleaning and drying can affect both the reliability of a power module and the testability of the finished circuit-board assembly. For guidance on appropriate soldering, cleaning and drying procedures, refer to *Board Mounted Power Modules: Soldering and Cleaning* Application Note.

## Through-Hole Lead-Free Soldering Information

The RoHS-compliant through-hole products use the SAC (Sn/Ag/Cu) Pb-free solder and RoHS-compliant

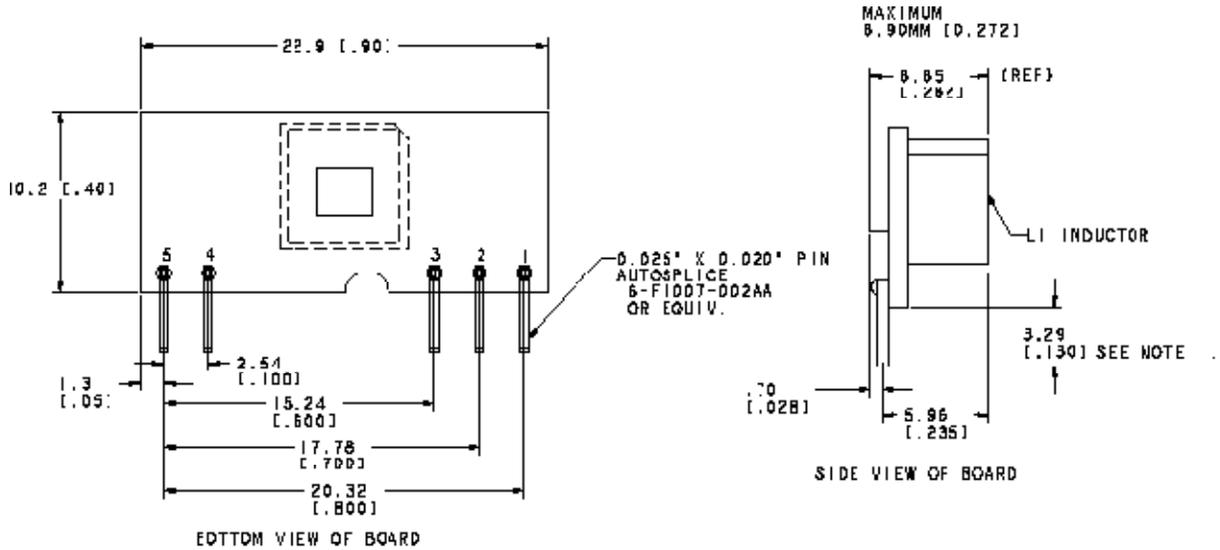
components. They are designed to be processed through single or dual wave soldering machines. The pins have an RoHS-compliant finish that is compatible with both Pb and Pb-free wave soldering processes. A maximum preheat rate of 3°C/s is suggested. The wave preheat process should be such that the temperature of the power module board is kept below 210°C. For Pb solder, the recommended pot temperature is 260°C, while the Pb-free solder pot is 270°C max. Not all RoHS-compliant through-hole products can be processed with paste-through-hole Pb or Pb-free reflow process. If additional information is needed, please consult with your Lineage Power technical representative for more details.

### Mechanical Outline

Dimensions are in millimeters and (inches).

Tolerances: x.x mm ± 0.5 mm (x.xx in. ± 0.02 in.) [unless otherwise indicated]

x.xx mm ± 0.25 mm (x.xxx in ± 0.010 in.)



PIN	FUNCTION
1	VOUT
2	TRIM
3	GND
4	VIN
5	ON/OFF

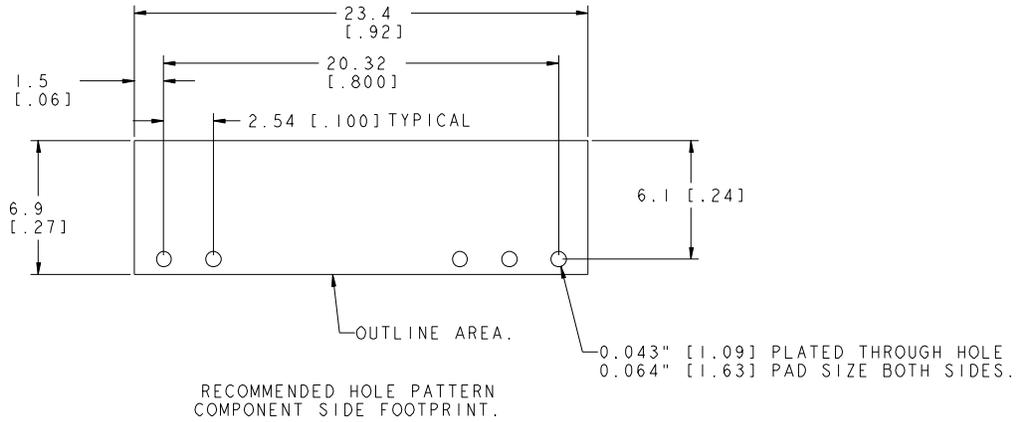
NOTE:  
LONG LEAD OPTION 5.08MM [0.200"]

### Recommended Pad Layout

Dimensions are in millimeters and (inches).

Tolerances: x.x mm ± 0.5 mm (x.xx in. ± 0.02 in.) [unless otherwise indicated]

x.xx mm ± 0.25 mm (x.xxx in ± 0.010 in.)



PIN	FUNCTION
1	VOUT
2	TRIM
3	GND
4	VIN
5	ON/OFF

## Ordering Information

Please contact your Lineage Power Sales Representative for pricing, availability and optional features.

**Table 3. Device Codes**

Device Code	Input Voltage Range	Output Voltage	Output Current	Efficiency 3.3V@ 5A	On/Off Logic	Connector Type	Comcodes
AXH005A0XZ	3.0 – 5.5Vdc	0.75 – 3.63Vdc	5 A	94.0%	Negative	SIP	CC109104881
AXH005A0X	3.0 – 5.5Vdc	0.75 – 3.63Vdc	5 A	94.0%	Negative	SIP	108979675

-Z refers to RoHS compliant Versions



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